## Model 560-197-12 56000 Data Rate Clock and Distribution System Chassis Specification

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## SECTION ONE

## 1. FUNCTIONAL DESCRIPTION

### 1.1 PURPOSE OF EQUIPMENT

The TrueTime Model 560-197-12 Data Rate Clock and Distribution System (DRC) Chassis provides volume distribution of Hughes Network Systems HNSTC custom time code. A total of eighteen (12) buffered distribution channels are provided with the option to expand in increments of six (6) channels up to a total of sixty (60).

Input signals are redundant and switchable while fault sense, switching and monitoring are provided via internal CPU. Hot Swappable redundant power supplies provide input power insuring improved availability of distribution.

### 1.1.1 PHYSICAL SPECIFICATIONS

Dimensions: $\quad 19 " w \times 5.22 " \mathrm{~h} \times 14$ "d ( $48 \mathrm{~cm} \times 13 \mathrm{~cm} \times 36 \mathrm{~cm}$ )
Weight: Approximately 13 pounds $(6 \mathrm{~kg})$

### 1.1.2 ENVIRONMENTAL SPECIFICATIONS

Operating Temp: $\quad 0^{\circ}$ to $+50^{\circ} \mathrm{C}$
Storage Temp: $\quad-40^{\circ}$ to $+85^{\circ} \mathrm{C}$
Humidity:
Up to $95 \%$ max., relative, non-condensing
Cooling Mode: Convection
Altitude Sea level to 10,000 feet

### 1.1.3 POWER SPECIFICATIONS

This specification reflects the signal supply stand alone power ratings.
Input Voltage: $\quad 100-240$ VAC (continuous), $47-63 \mathrm{~Hz}$
Input Power: $\quad 135 \mathrm{~W}$ maximum
Fuse $\quad 3$ Amp 3AG SLO-BLO
Output Power: See table below
Output Voltage: $\quad 56$ VDC $\pm 5 \%$
Maximum Output Power:

| INPUT | AMBIENT | AIRFLOW | OUTPUT |
| :---: | :---: | :---: | :---: |
| $100-240$ VAC | $50^{\circ} \mathrm{C}$ | CONVECTION | 90 W |

Maximum Poweras Configured: 30 Watts *
*add 5 Watts for each group of 6 HNSTC outputs beyond the configured 18.
1.1.4 CONNECTOR SPECIFICATIONS
Location: Power Entry Module
Mating Connector: IEC320 Connector
1.2 INPUT/ OUTPUT
1.2.1 HNSTC
Signal Type: Balanced RS-422 Serial Time Code
Connector Type: RJ-12
1.2.2 CPU INPUT/OUTPUT
Connector Type: ..... DB-9M
Connector Pinout:
Pin 1: OUT 1
Pin 2: $\quad$ RXD / RS-232 In
Pin 3: TXD / RS-232 Out
Pin 4: OUT 2
Pin 5: SIGNAL GND
Pin 6: RIN- / Differential- In
Pin 7: $\quad$ RIN+ / Differential+ In
Pin 8: TOUT- / Differential- Out
Pin 9: TOUT+ / Differential+ OutSerial
RS-232/422 selectable
1.3 DISTRIBUTION
1.3.1 HNSTC
Connector Type: RJ-12
Signal Type: RS-422 OUTPUTS
Quantity: 18 pairs (Optional up to 60 pairs)Signal Type:Amplitude:2.8 Vpp into 100 ohms
Signal Delay: ..... $<60 \mathrm{~ns}$
Output Drive Compliance:MIL-STD-188-124A TYPE II BALANCEDRS-422-A

### 1.4 CERTIFICATIONS

### 1.4.1 CE COMPLIANT

This unit is CE compliant. Declaration of Conformity is included at the front of this product manual in the form of an addendum.

Conditions for acceptability:

1. The unit must be mounted in a rack or other device, which contains adequate bottom containment.
2. If the unit is operated over 140 V input at up to 80 W input, forced air cooling of 120 CFM must be provided to the bottom of the unit. If the device is operated over 140 V input at up to 110 W input, 210 CFM must be provided to the bottom of the unit.

### 1.4.2 UR AND C-UR RECOGNIZED

This unit is UR and C-UR recognized and shall be marked as such. The conditions for acceptability include the requirement that the unit be installed in a rack or other device that provides adequate bottom containment. Other conditions for acceptability are established by the power supplies (model 560-5217) used within this configuration are listed below.

Power Supply 560-5217 conditions of acceptability:
The use of the 560-5217 power supply is CE compliant and UR and C-UR recognized.

### 1.5 DEMONSTRATED MEAN TIME BETWEEN FAILURE (MTBF)

MTBF: Similar configuration of model 560-197-X demonstrate 408966 Hours based on 118 units in field for 68161 days and 4 returns.


FRONT VIEW

| CONTRACT NO. |  |  | TRUETIME |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| APProvalis |  | DATE |  |  |  |  |  |  |
| DRAWN EY | RNR | 05/01 | 56K SIGNALDISTRIBUTION CHASSIS |  |  |  |  |  |
| CHECKD ${ }^{\text {aY }}$ |  |  |  |  |  |  |  |  |
| APPROVED 㫙 | 16 | 09/01 |  |  |  |  |  |  |
| NEXT ASSY |  |  |  |  |  |  |  |  |
|  |  |  | SCALE | NONE |  | ISHEET 1 Of |  |  |



$\qquad$

| SLOT | FRONT | REAR |
| :---: | :---: | :---: |
| 1 | BLANK |  |
| P/N: $560-1107$ | RS-422 INTERFACE <br> TIMING INPUT MODULE <br> P/N: 560-5283 |  |
| 2 | BLANK | RS-422 INTERFACE |
|  | P/N: 560-1107 | TIMING INPUT MODULE |
| P/N: 560-5283 |  |  |

# ORIGINAL 

## Assembly Switch Summary

 For Chassis 560-197-121. Chassis 560-197-2 Backplane switch settings:

| Assembly <br> Switch <br> Designator | Switch <br> position <br> $\mathbf{1}$ | Switch <br> position <br> $\mathbf{2}$ | Switch <br> position <br> $\mathbf{3}$ | Switch <br> position <br> $\mathbf{4}$ | Switch <br> position <br> $\mathbf{5}$ | Switch <br> position <br> $\mathbf{6}$ | Switch <br> position <br> $\mathbf{7}$ | Switch <br> position <br> $\mathbf{8}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 560-5165 (Back <br> plane Assembly) <br> SW1 | OFF | OFF | OFF | OFF | OFF | OFF | OFF | OFF |

2. Front Card slots 4, 6: 560-5181-2 (DIGITAL PRI/SEC SWITCH/DRIVER) IRIG B RS-422

| Assembly Switch Designator | Switch position 1 | Switch position 2 | Switch position 3 | Switch position 4 | Switch position 5 | Switch position 6 | Switch position 7 | Switch position 8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SW1 | ON | OFF | OFF | OFF |  |  |  |  |
| SW2 | OFF | ON | OFF | OFF |  | Wexak |  |  |
| SW3 | OFF | OFF | ON | OFF |  |  |  |  |
| SW4 | OFF | OFF | OFF | OFF | OFF | OFF | OFF | OFF |
| SW5 | ON | OFF | OFF | OFF | OFF | OFF | OFF | OFF |
| SW6 | OFF | ON | OFF | OFF | OFF | OFF | OFF | OFF |
| SW7 | ON | ON | ON | ON | ON | ON | ON | OFF |

## 3. Front Card slot 17: 560-5179-1 (Fault/Status Monitor CPU)

| Assembly Switch Designator | Switch position 1 | Switch position 2 | Switch position 3 | Switch position 4 | Switch position 5 | $\begin{aligned} & \hline \text { Switch } \\ & \text { position } \end{aligned}$ | Switch position 7 | Switch position 8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SW1 | ON | ON | ON | OFF |  |  |  |  |
| SW3 | ON | OFF | OFF | OFF | 4 ${ }^{4}$ |  |  |  |

## 4. FRONT Card slots 18-19: 560-5217 (AC Power Supply) NO USER SWITCHES

5. FRONT Card slots 20-21: 560-5217 (AC Power Supply) NO USER SWITCHES
6. Rear Card slot 1: 560-5283 RS-422 INTERFACE INPUT MODULE (IRIG B RS-422)

| Assembly <br> Switch <br> Designator | Jumper <br> position <br> $1-2$ | Jumper <br> position <br> $3-4$ | Jumper <br> position <br> $5-6$ | Jumper <br> position <br> $7-8$ | Jumper <br> position <br> $9-10$ | Jumper <br> position <br> $11-12$ | Jumper <br> position <br> $13-14$ | Jumper <br> position <br> $15-16$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| JP1 | X |  |  |  |  |  |  |  |
| JP2 |  |  |  |  |  |  |  |  |
| JP3 |  |  |  |  |  |  |  |  |

7. Rear Card slot 2: 560-5283 RS-422 INTERFACE INPUT MODULE (IRIG B RS-422)

| Assembly <br> Switch <br> Designator | Jumper <br> position <br> $1-2$ | Jumper <br> position <br> $3-4$ | Jumper <br> position <br> $5-6$ | Jumper <br> position <br> $7-8$ | Jumper <br> position <br> $9-10$ | Jumper <br> position <br> $11-12$ | Jumper <br> position <br> $13-14$ | Jumper <br> position <br> $15-16$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| JP1 |  |  |  |  |  |  |  |  |
| JP2 |  | X |  |  |  |  |  |  |
| JP3 |  |  |  |  |  |  |  |  |

8. Rear Card slots 4, 6: 560-5141-5 (Passive Output I/F RJ11) NO USER SWITCHES
9. Rear Card slot 17: 560-5187-1T (Alarm I/O Module) NO USER SWITCHES

## 56K CHASSIS PROGRAMMING FOR SYSTEM 560-197-12

ENTER THE FOLLOWING COMMANDS TO PROGRAM THE CPU CARD:

```
TPRI=P
TSEC=S
PSRC=D
SSRC=D
TSRC=O
SITE=nn ;where nn = Chassis Number 02 -99
PROFF
AROFF
PRI
REF
```

| Parent llem Component Item | Parenl Description Component Descripiton | Batch Quantily Quantly Per |  | Bubble <br> Seq No |  | Level |  |  |  |  | Effec |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | UM |  | Remarks |  |  | Ty | Seq | I | From | Thru |
| 560-197-12 | SIGNAL DISTRIB CHASSIS |  | EA | Typa | M - Rev_-34- | Draw | 560 | 197- |  |  |  |  |
| 0000-PL | PARTS LIST REV LEVEL | 1.00 | EA |  | REV B (05-09-02) |  | 1 | 5 | 2.0 | M | 1/1/2000 | 12/31/2010 |
| 0000-PRINT | REFERENCE PRINT | 1.00 | EA |  | 560-197-12 REV B |  | 1 | S | 3.0 | M | 1/1/2000 | 12/31/2010 |
| 212-011 | AIR FLOW BARRIER 4HP (1 SLOT) | 20.00 | EA | 2 |  |  | 1 | S | 14.0 | P | 5/1/2002 | 12/31/2010 |
| 560-1107 | ASSY FRT PNL BLANK . 8 IN . | 14.00 | EA |  |  |  | 1 | S | 4.0 | P | 1/1/2000 | 12/31/2010 |
| 560-1181-6 | ASSY,REAR PANEL | 12.00 | EA |  |  |  | 1 | S | 5.0 | P | 1/1/2000 | 12/31/2010 |
| 560-1222-2 | ASSY AC PWR ENTRY MODULE | 0.00 | EA |  | REF. (PART OF 560-197-2) |  | 1 | s | 13.0 | M | 9/14/2000 | 12/31/2010 |
| 560-197-2 | SIGNAL DISTRIB CHASSIS | 1.00 | EA |  |  |  | 1 | S | 6.0 | M | 1/1/2000 | 12/31/2010 |
| 560-5141-5 | PASSIVE OUT INTFC,RJ11 | 2.00 | EA |  |  |  | 1 | $\mathbf{S}$ | 7.0 | M | 1/1/2000 | 12/31/2010 |
| 560-5179-1 | ASSY FAULT MONITOR/CPU | 1.00 | EA |  |  |  | 1 | S | 8.0 | M | 1/1/2000 | 12/31/2010 |
| 560-5181-2 | DC SWITCHITIMING DRVR,HEX | 2.00 | EA |  |  |  | 1 | S | 9.0 | M | 1/1/2000 | 12/31/2010 |
| 560-5187-1T | CPU IIO MODULE, QUAD INPUT | 1.00 | EA |  |  |  | 1 | S | 10.0 | M | 1/1/2000 | 12/31/2010 |
| 560-5217 | ASSY 56K PWR SUPPLY | 0.00 | EA |  | REF. (PART OF 560-197-2) |  | 1 | 5 | 12.0 | M | 9/14/2000 | 12/31/2010 |
| 560-5283 | ASSY 56K REAR CONN RS422 | 2.00 | EA |  |  |  | 1 | 5 | 11.0 | M | 1/1/2000 | 12/31/2010 |

